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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *None* *dc*
 \*\* FOREIGN APPLICATIONS \*\*\*\*\* *Yes* *dc*  
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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials	JAPAN	13	11	1

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## TITLE

Semiconductor memory device including MOS transistor having a floating gate and a control gate

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